IN THE SPECIFICATION

Page 8, the third full paragraph:

In the second amplification circuit unit 80, the twelfth transistor Q12 receives and amplifies the output signal of the eighth transistor Q8 matched with the third capacitor C3 which is an intermediate impedance matching element, and outputs the amplified high-frequency signal to an antenna 40 through an output terminal ~~Vcc2_OUT~~ VCC2_OUT of the second amplification circuit unit. The fifth capacitor C5 is an element for reducing an output high-frequency component of the second amplification circuit unit.

Page 9, the first full paragraph:

If a base current of the second transistor Q2 is ignored, a collector current of the first transistor Q1 is the same as a current L_c1 I_c1 supplied to a forth resistor R4 as shown in Equation 1 as follows:

Page 10, the second full paragraph:

A collector current I_c3 of the fourth transistor is determined by the reference current ~~L_c2~~ I_c2 and emitter resistance ratio of the third transistor Q3 due to a current mirror relationship between the third transistor Q3 and the fourth transistor Q4 as follows:

Page 11, the fifth full paragraph:

The current I_2 is supplied from the supply voltage terminal ~~Vcc2_OUT~~ VCC2_OUT. A voltage of the supply voltage terminal ~~Vcc2_OUT~~ VCC2_OUT in the second amplification circuit unit is the same as a supply voltage Vcc of the supply voltage terminal VCCO 71.

Page 12, the sixth full paragraph:

Therefore, in a case of not including the first transistor Q1, a collector voltage VC1 of the first ~~trnasister~~ transistor Q1 is determined as follows: